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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Deshanand Singh et al.	Examiner: Tat, Binh C
Serial No.:	10/679,593	Group Art Unit: 2825
Filed:	October 6, 2003	Docket No.: ALT.P024 (A01002)
Title:	Method and Apparatus for Performing Layout-Driven Optimizations on Field Programmable Gate Arrays	

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE

Dear Sir,

In response to the Office Action mailed on May 13, 2008, Applicants respectfully request the Examiner to consider the following response. Submitted herewith is a petition for a 1 month extension of time. Please charge any deficiencies in those fees, or credit any overpayment to Deposit Account 50-1624.

PAGE 4/17 * RCVD AT 9/15/2008 9:25:14 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-5/35 * DNIS:2738300 * CSID:2173567373 * DURATION (mm-ss):03-02